

U.S. **UTILITY** Patent Application

Ed  
O.I.P.E.  
SCANNED Ed L O.A. TM

PATENT DATE

APPLICATION NO. 09/864445	CONT/PRIOR F	CLASS <del>327</del> 257	SUBCLASS 091	ART UNIT <del>2846</del> 2827	EXAMINER THAT
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5 Minami Iwamoto

Semiconductor integrated circuit having functional macro with improved power line connection structure

PTO-2040  
12/99

**ISSUING CLASSIFICATION**

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig. <div style="font-size: 2em; text-align: center;">7</div>	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Primary Examiner) _____ (Date)		<b>ISSUE FEE</b>	
Amount Due			Date Paid	
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